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Figure 6 is a diagram depicting the saving of data from VRAM to a buffer in accordance with a preferred embodiment of the present invention;

Figure 7 is a flowchart of a process for restoring
5 the contents of a video graphics adapter in accordance with a preferred embodiment of the present invention;

Figures 8A-8C are illustrations of pseudo code employed in a device driver when an application switches to the foreground;

10 Figures 9A-9C are illustrations of pseudo code employed in a device driver when an application switches to the background;

Figure 10 is a high level flowchart of a process for capturing I/O ports in accordance with a preferred
15 embodiment of the present invention;

Figure 11 is shown below in Figures 11A-11B, which are diagrams of pseudo code used to capture port addresses in accordance with a preferred embodiment of the present invention; and

20 Figure 12A-12C are diagrams of pseudo code used to set modes in accordance with a preferred embodiment of the present invention.